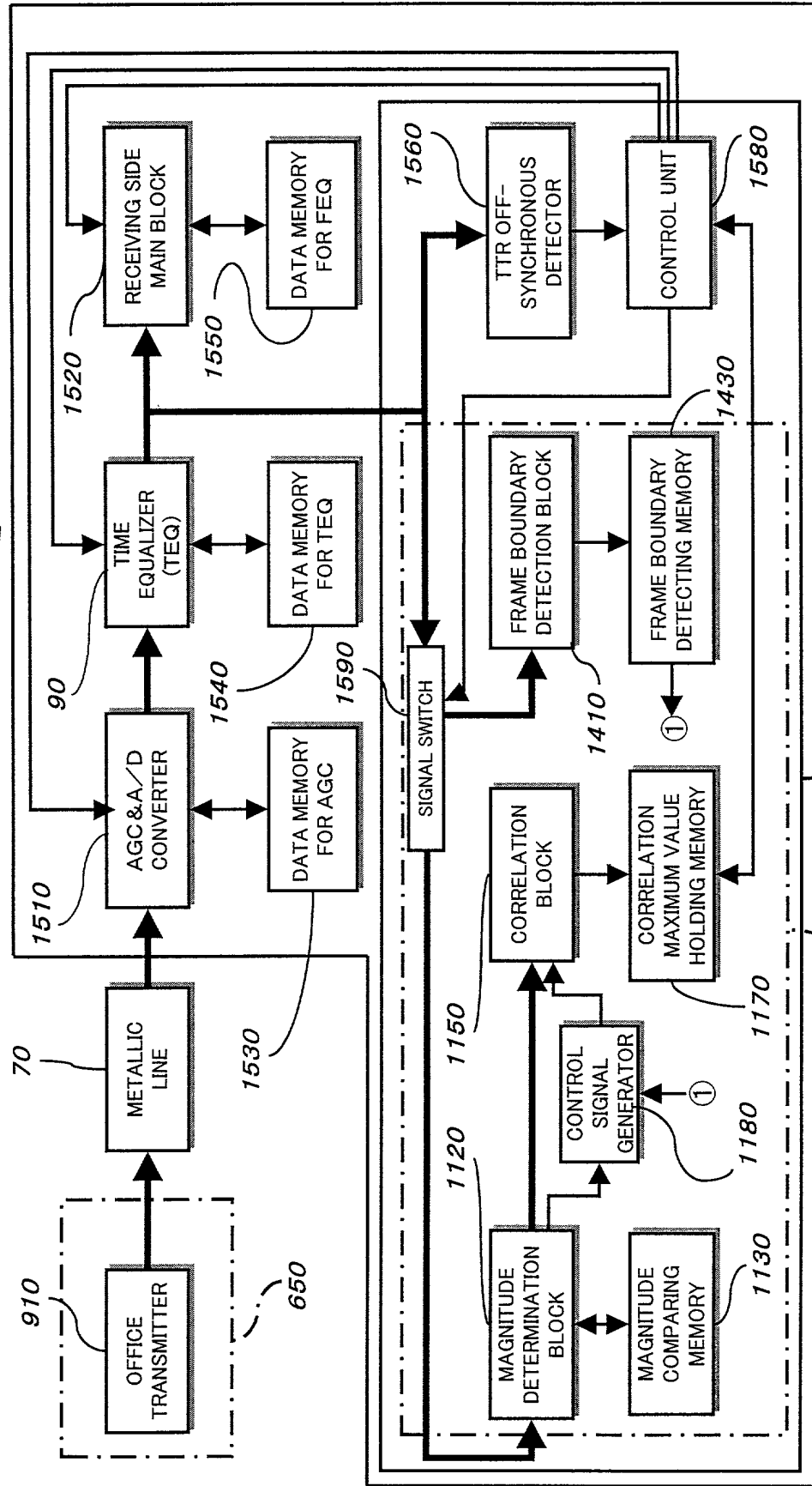


FIG. 1

1: SUBSCRIBER ADSL MACHINE



3: TTR RESYNCHRONOUS CONTROLLER

1570: SIGNAL DETECTION PROCESSOR

FIG. 2

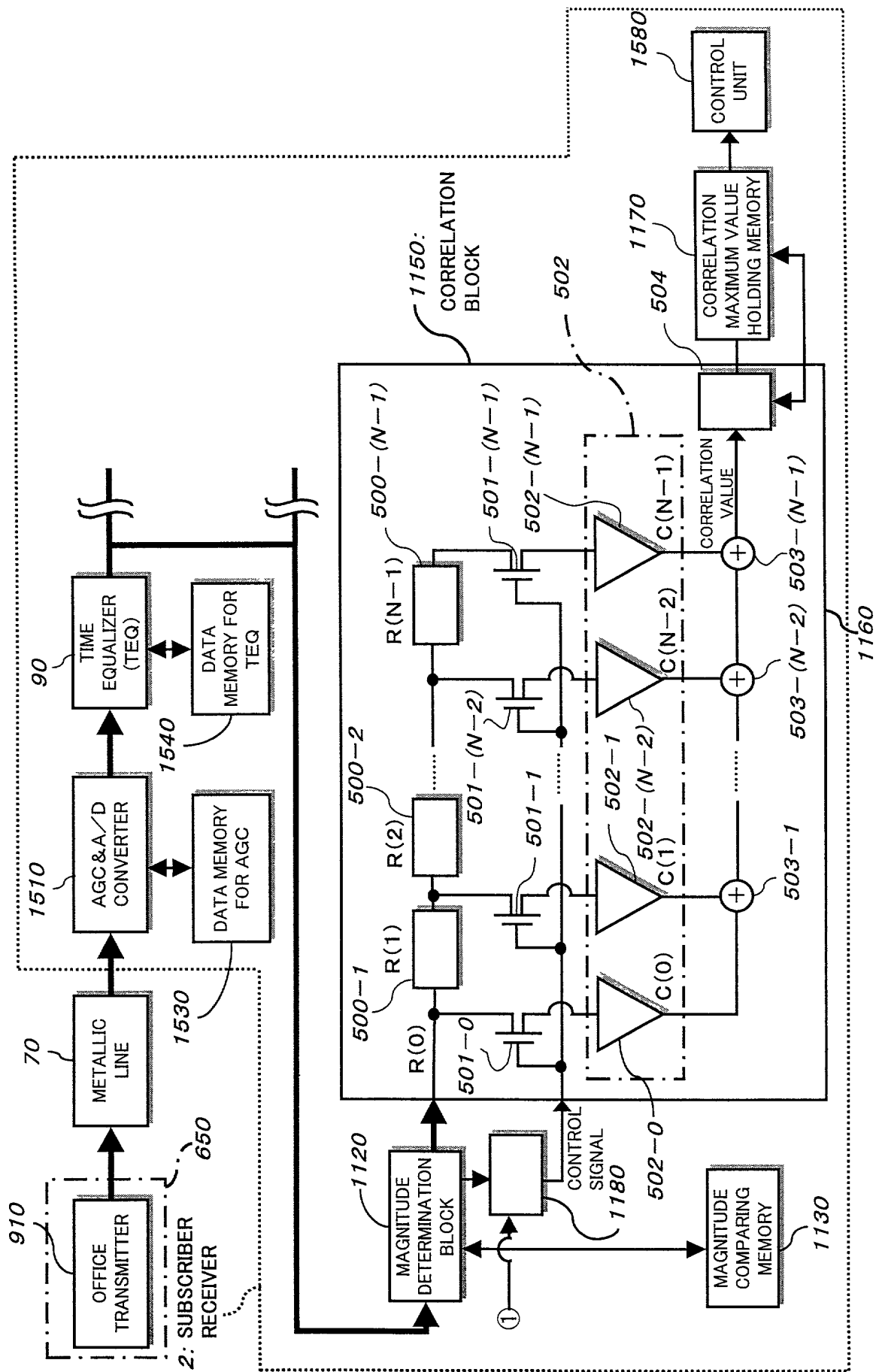


FIG. 3

1130: MAGNITUDE COMPARING MEMORY

MEMORY NUMBER	DETERMINATION RESULT
J(0)	1
J(1)	1
J(2)	1
⋮	
J(N-2)	1
J(N-1)	1

1131: DETERMINATION RESULT HOLDING MEMORY

FIG. 4

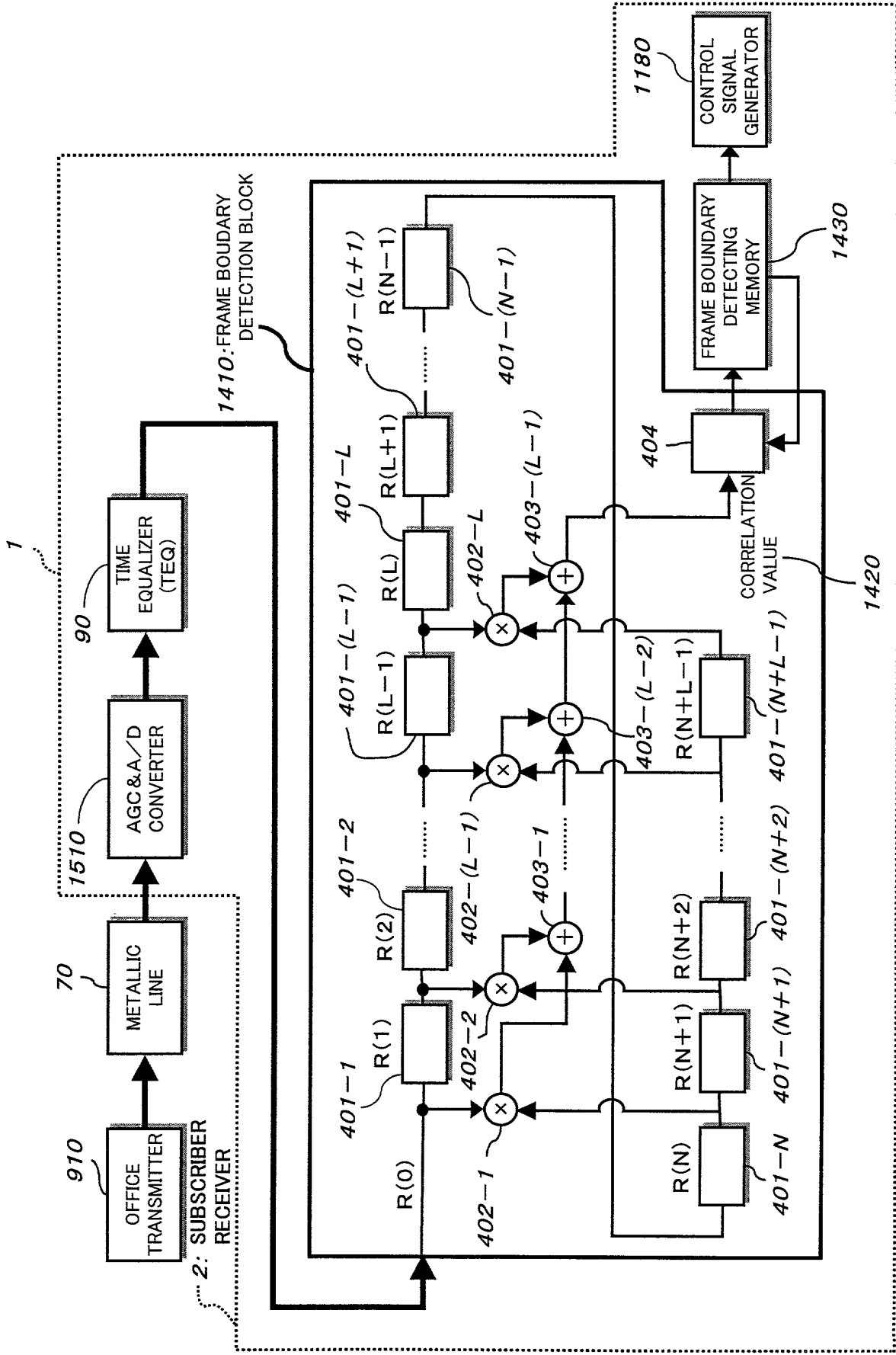
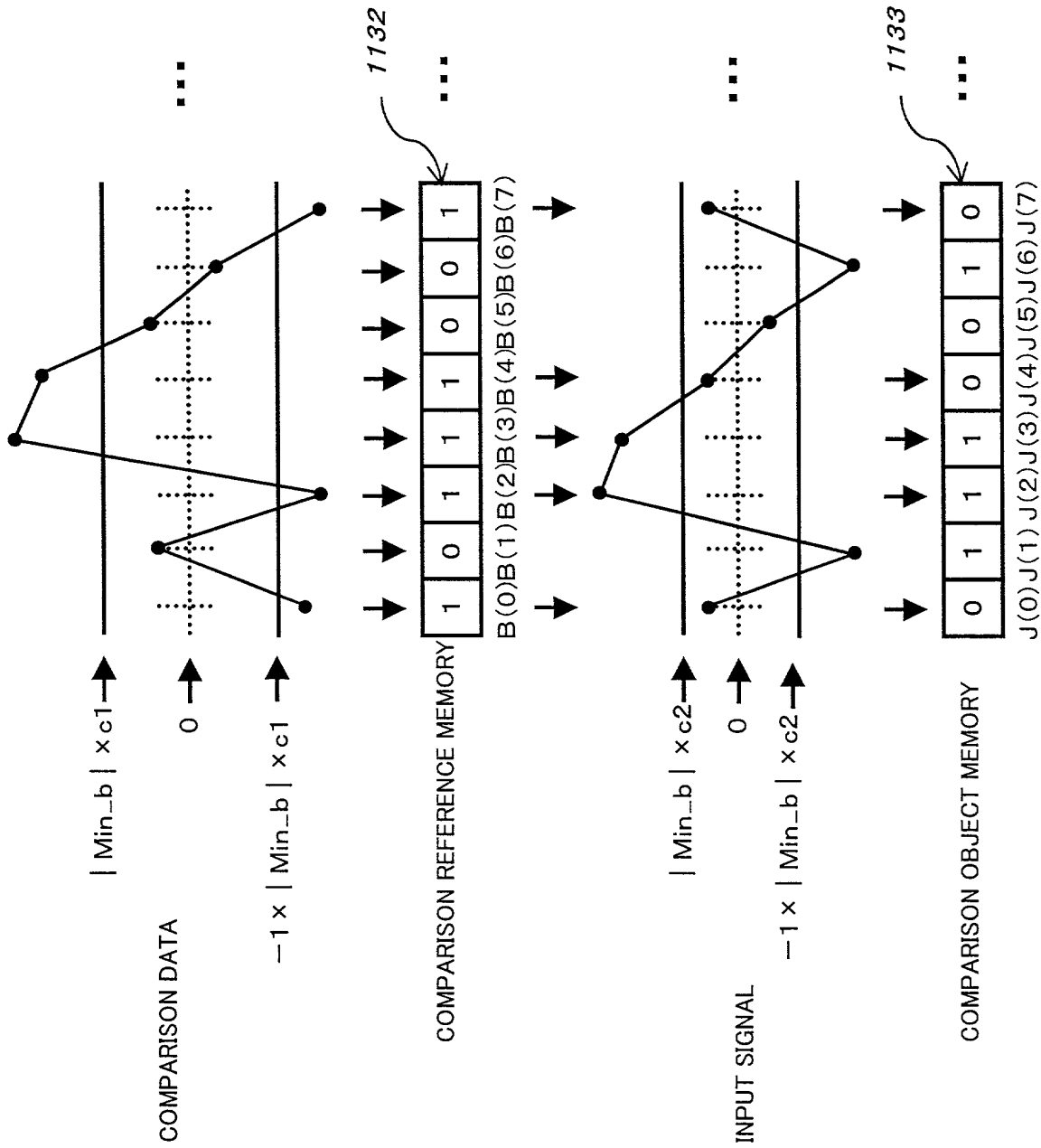
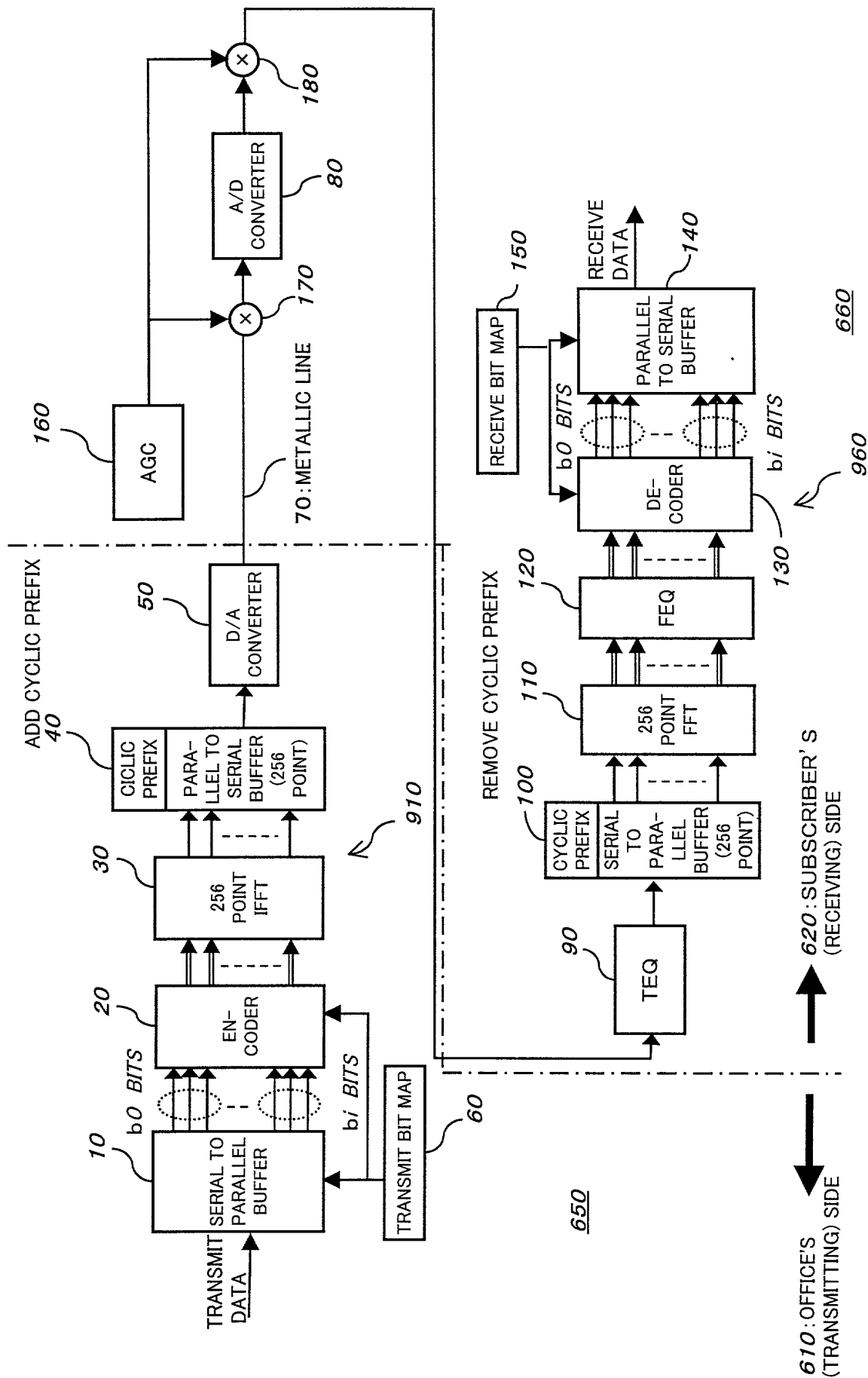


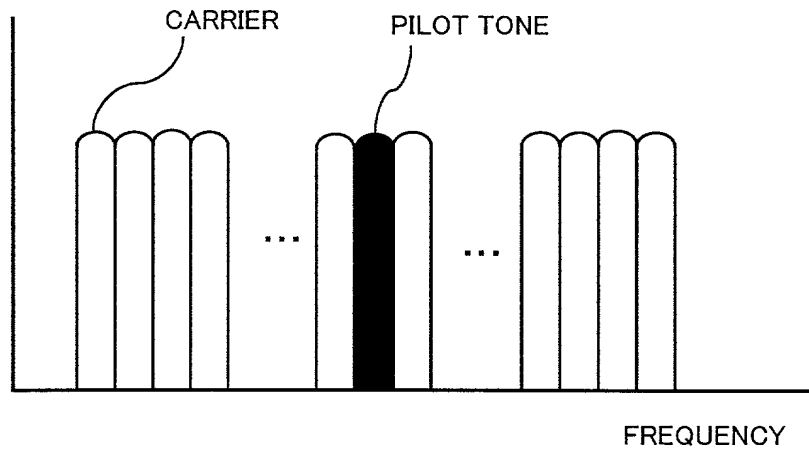
FIG. 5



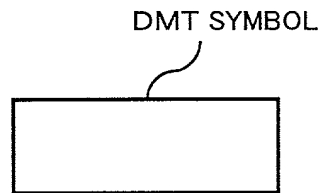
**FIG. 6**  
RELATED ART



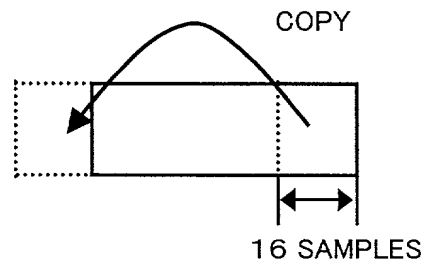
**FIG. 7**  
RELATED ART



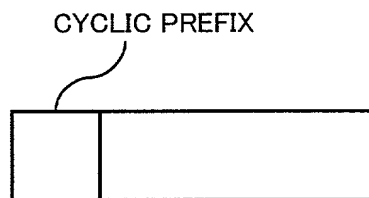
**FIG. 8(A)**  
RELATED ART



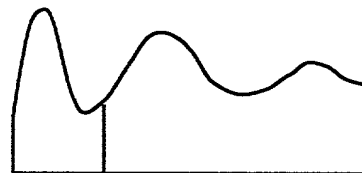
**FIG. 8(B)**  
RELATED ART



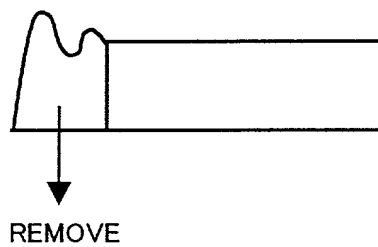
**FIG. 8(C)**  
RELATED ART



**FIG. 8(D)**  
RELATED ART



**FIG. 8(E)**  
RELATED ART

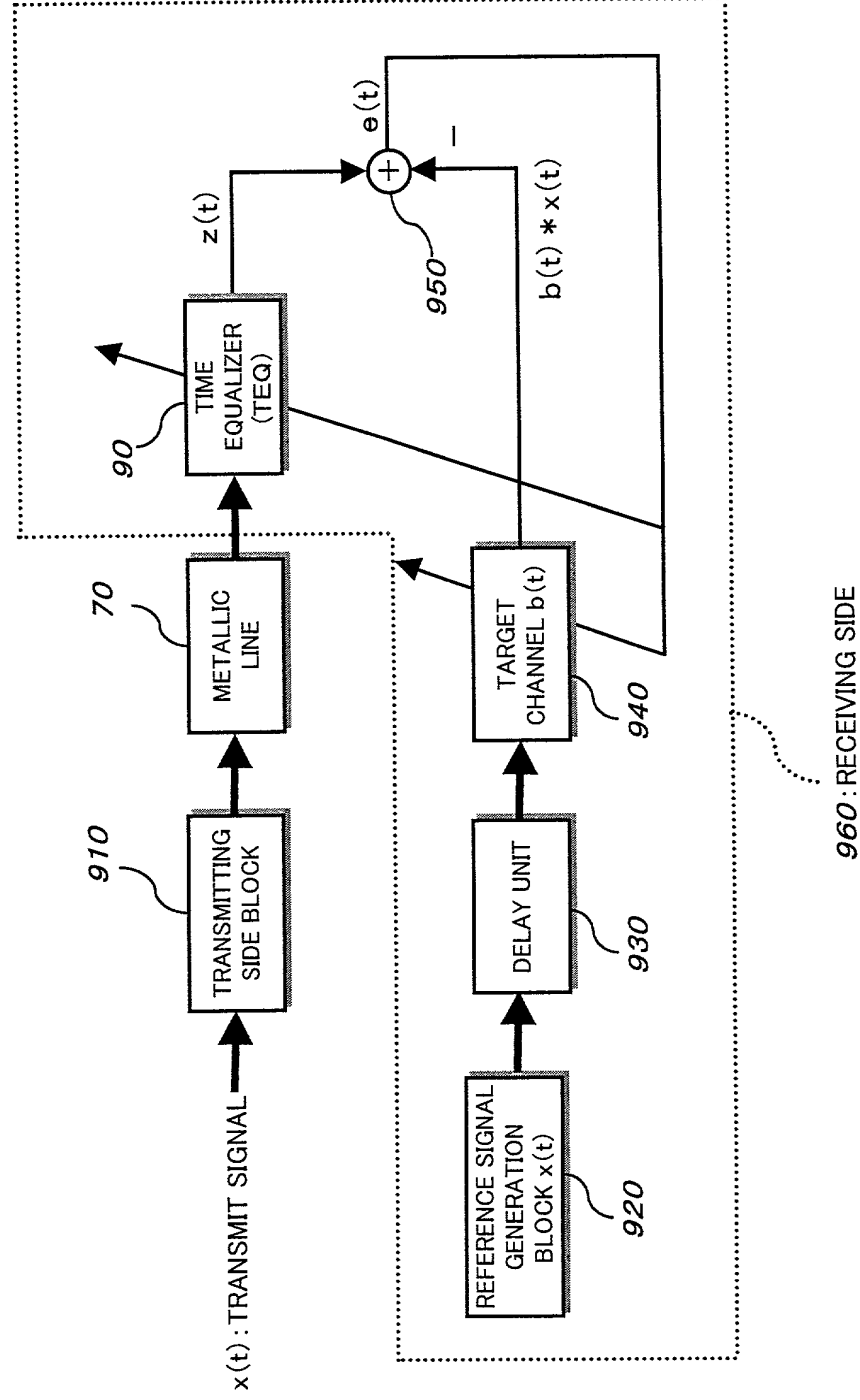


**FIG. 8(F)**  
RELATED ART

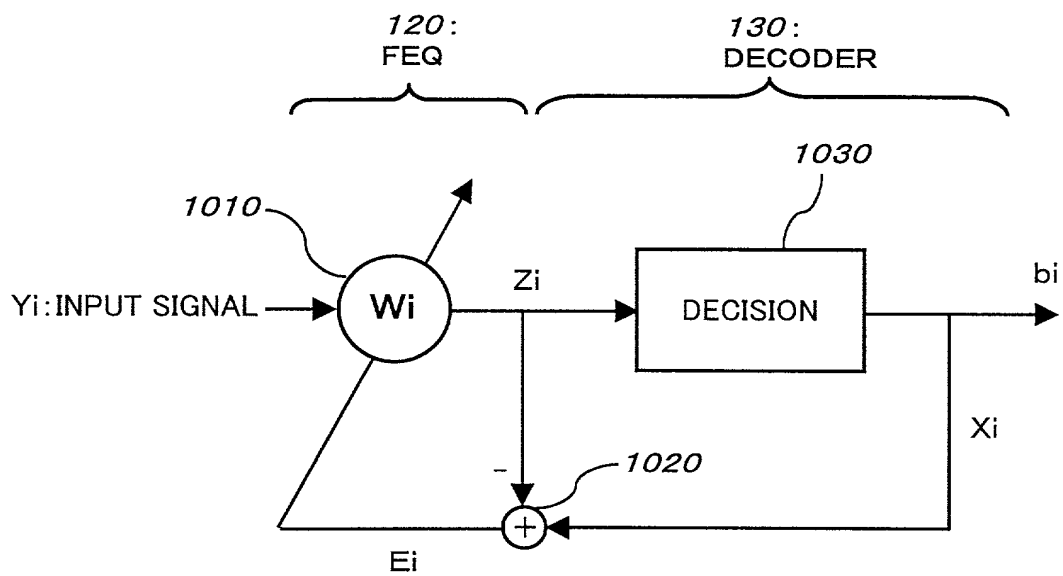




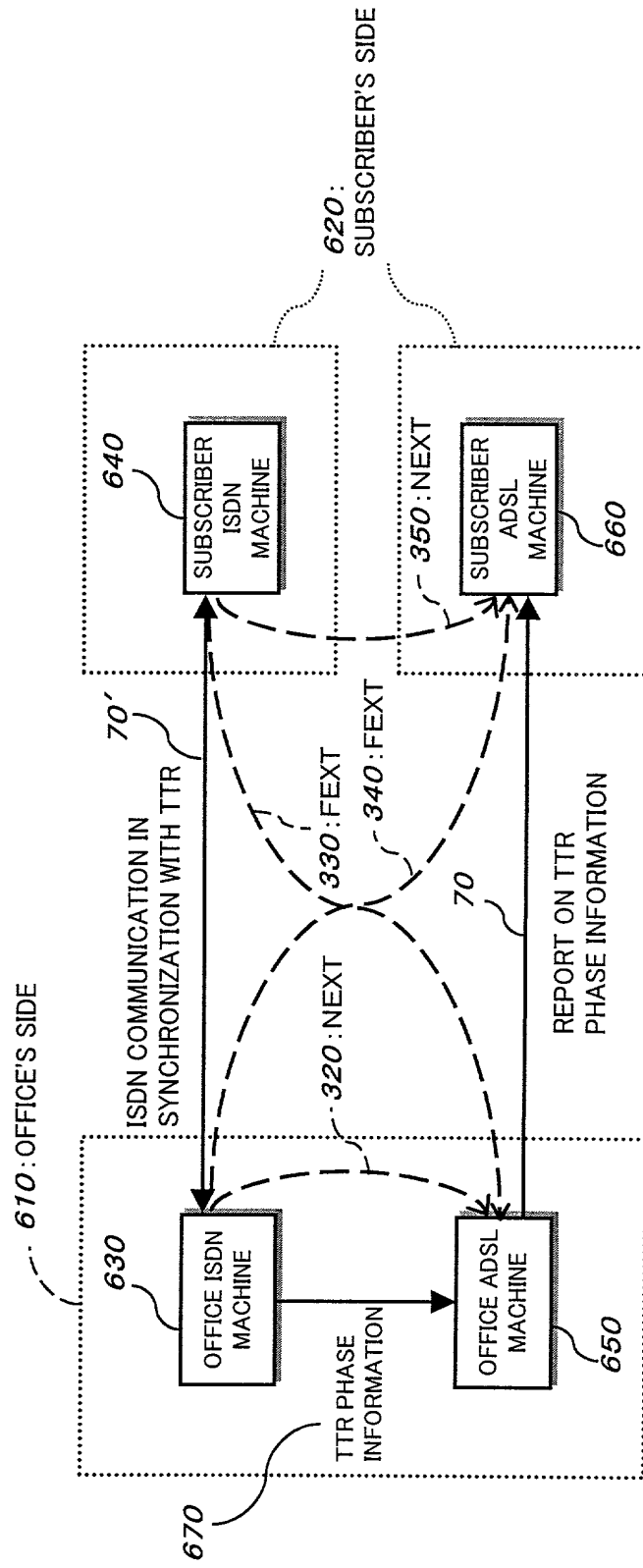
**FIG. 9**  
RELATED ART

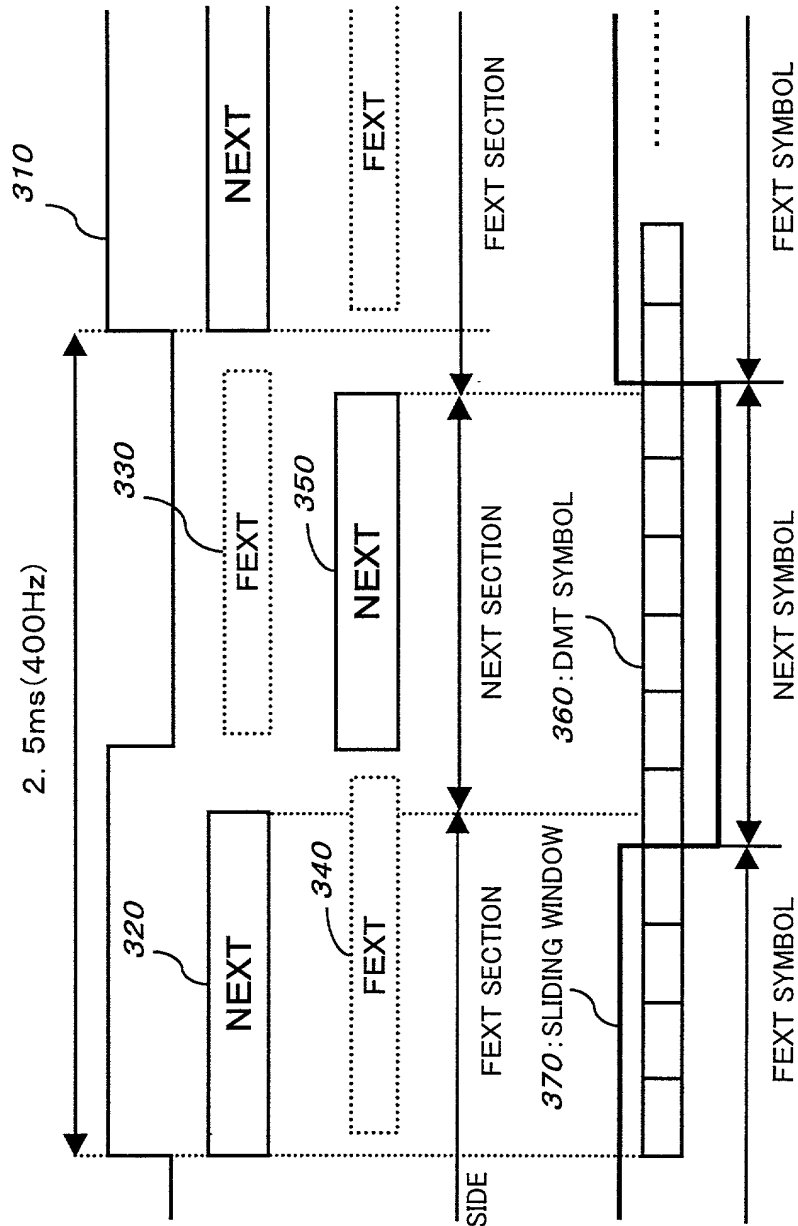


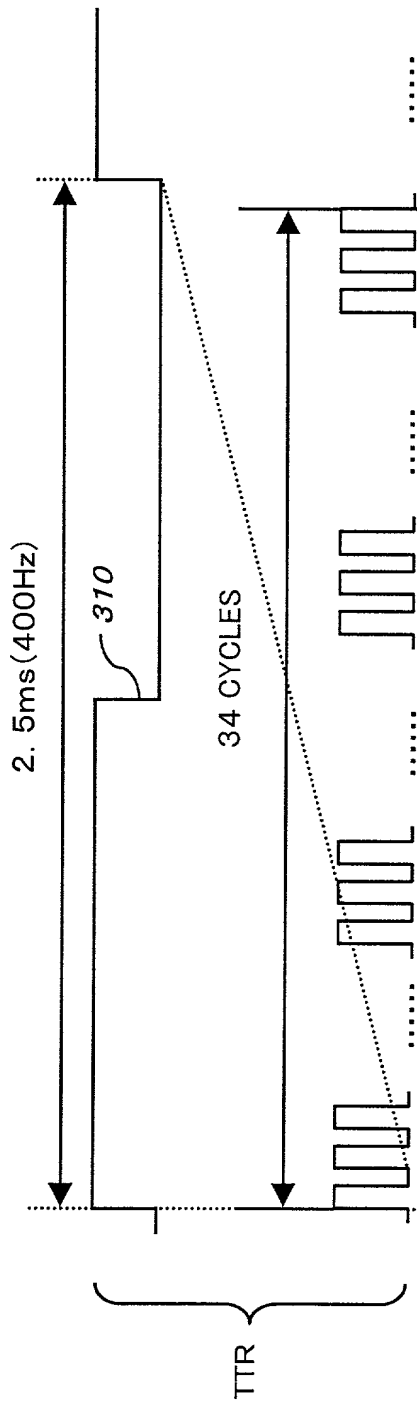
**FIG. 10**  
RELATED ART



**FIG. 11**  
RELATED ART

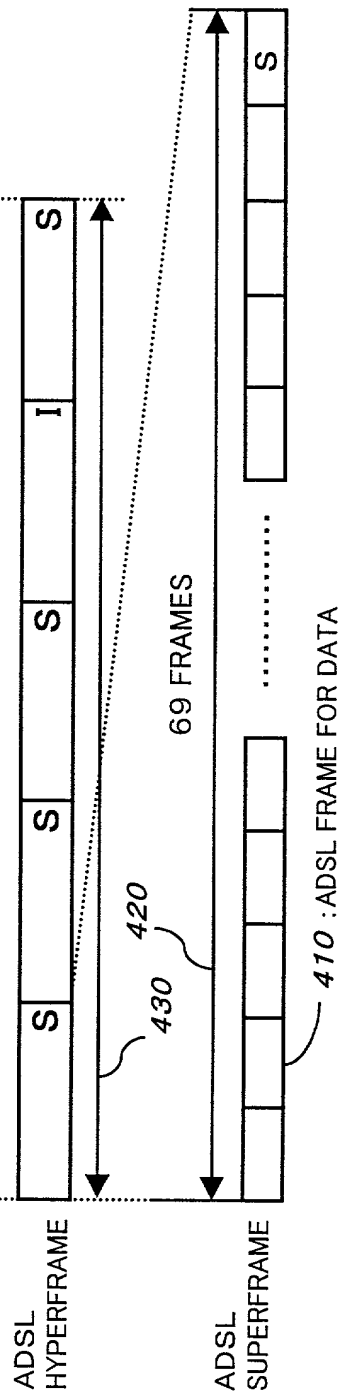






**FIG. 13(A)**  
RELATED ART

**FIG. 13(B)**  
RELATED ART



**FIG. 13(C)**  
RELATED ART

S...SYNCHRONIZATION SYMBOL

I...INVERSE SYNCHRONAIZATION SYMBOL

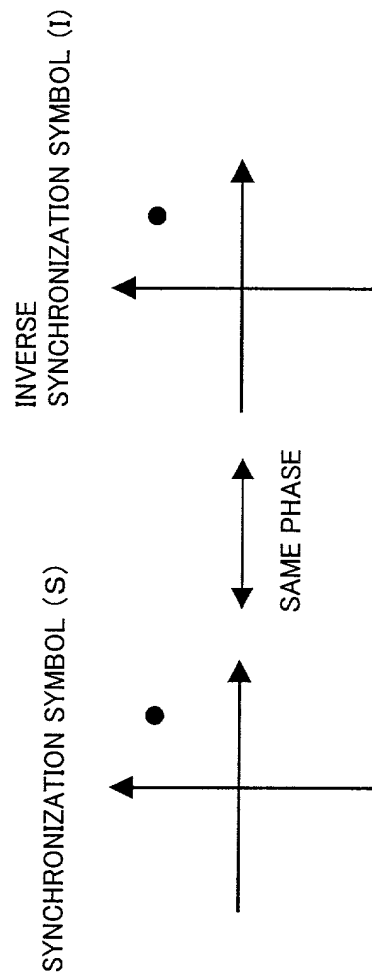


FIG. 14(A)  
RELATED ART

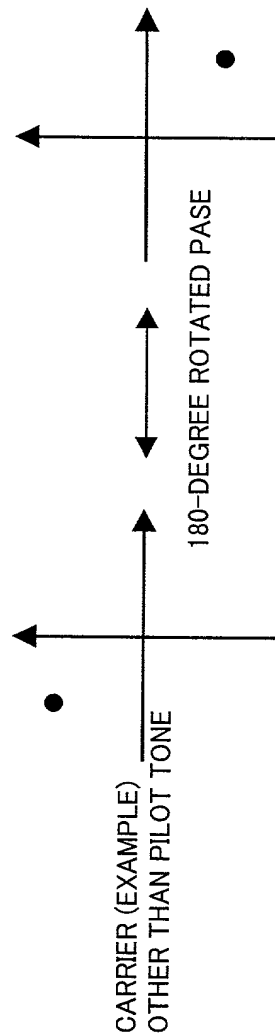
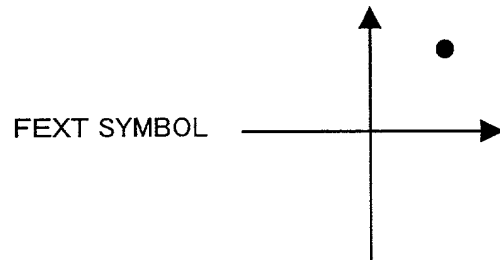


FIG. 14(B)  
RELATED ART

**FIG. 15(A)**  
RELATED ART



**FIG. 15(B)**  
RELATED ART

